

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Michail Konstantinos Tsatsanis, et al.)	Group Art Unit 2661
App. No.	:	10/658,117)	
Filed	:	September 8, 2003)	Thereby certify that this correspondence and all marke attachments are being deposited with the United States Post Service as first-class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Albert dia, VA 22313
For	:	DECISION FEEDBACK TRANSCEIVER FOR MULTICHANNEL COMMUNICATION SYSTEM))))	February 13, 2004 Chad W. Miller, Reg. No. 44,943
Examiner	:	Unknown) _)	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. This Information Disclosure Statement is being filed before the mailing of the first Office Action on the merits, and as such, no fee is required in accordance with 37 C.F.R. § 1.97(b)(3).

Dated: 2/13/04

Respectfully submitted,

Chad W. Miller

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		SHEET 1 OF		
FORM PTO-109 U.S. DEPARTMENT OF COMMERCE	ATTY. DOCKET NO.	APPLICATION NO.		
PATENT AND TRADEMARK OFFICE	AKTINO.0002P	10/658,117		
FEB 1 7 2004 범	APPLICANT			
INFORMATION DISCLOSURE STATEMENT	Michail Konstantinos Tsatsanis, et al.			
BY APPLICANT	FILING DATE	GROUP		
(USE SEVERAL SHEETS IF NECESSARY)	September 8, 2003	2661		

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U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	5,285,474	02/1994	Chow, et al.			
	5,479,447	12/1995	Chow, et al.			
	5,673,290	09/1997	Cioffi			
	5,887,032	03/1999	Cioffi			
	US 2003/0086362 A1	05/2003	Hasegawa, et al.			

· A	FOREIGN PATENT DOCUMENTS						
EXAMINER	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANS	LATION
INITIAL						YES	NO
							·

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)				
	Eric W. Weisstein, "Eric Weisstein's World of Mathematics - Diagonal Matrix", ©1999 CRC Press LLC, ©1999-2003 Wolfram Research, Inc., 2 pages				
Steffen Trautmann, et al., "A New Equalizer for Multitone Systems Without Guard Time", IEEE Communications Letter January 2002, pp. 34-36					
	Jeffrey Wu, et al., "Efficient Matrix Multiplication Methods to Implement a Near-optimum Channel Shortening Method for DMT Transceivers", The University of Texas at Austin, Asilomar Conference, October 30, 2000, 14 pages				
	J. Cioffi, "Digital Duplexing: VDSL Performance Improvement by Aversion of Frequency Guard Bands (TD13)", ETSI TM6 Endinburgh, September 20-24, 1999, pp. 1-16				
	Lei Wang, et al., "Low-Power AEC-Based MIMO Signal Processing for Gigabit Ethernet 1000Base-T Transceivers", Copyright 2001 ACM 1-58113-371-5/01/0008, pp, 334-339				
	Arthur Redfern, et al., "Expanding the Reach of ADSL", http://www.commsdesign.com/story.OEG20030717S0028, July 17, 2003, 2 pages				

EXAMINER	DATE CONSIDERED